## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

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Title of Invention

REDUNDANCY STRUCTURE AND METHOD FOR HIGH-SPEED SERIAL LINK

Application Number : Confirmation Number:

First Named Applicant: Louis Hsu

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or 5912579 or 5943223 or 6037719 or 6043727 or 6049702 or 6069587 or 6127744 or 6131169 or 6143997 or 6160230 or 6275890 or 6307519 or 6310339 or 6310526

or 6396368 or 6473361 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4811275	1989-03-07	Balogh, Jr. et al.			
	2	5541614	1996-07-30	Lam et al.			
	3	5638946	1997-06-17	Zavracky			
	4	5757319	1998-05-26	Loo et al.			
	5	5808527	1998-09-15	De Los Santos			
	6	5834975	1998-11-10	Bartlett et al.			
	7	5872489	1999-02-16	Chang et al.			
	8	5912579	1999-06-15	Zagar et al.			
	9	5943223	1999-08-24	Pond			
	10	6037719	2000-03-14	Yap et al.			
	11	6043727	2000-03-28	Warneke et al.			
	12	6049702	2000-04-11	Tham et al.			
	13	6069587	2000-05-30	Lynch et al.			
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	17	6160230	2000-12-12	McMillan et al.			
	18	6275890	2001-08-14	Lee et al.			
	19	6307519	2001-10-23	Livingston et al.			
	20	6310339	2001-10-30	Hsu et al.			
	21	6310526	2001-10-30	Yip et al.			

	22	6396368	2002-05-28	Chow et al.		
	23	6473361	2002-10-29	Chen et al.		

## Signature

Examiner Name	Date